



BISS0001 High performance Sensing Signal Processing Chip

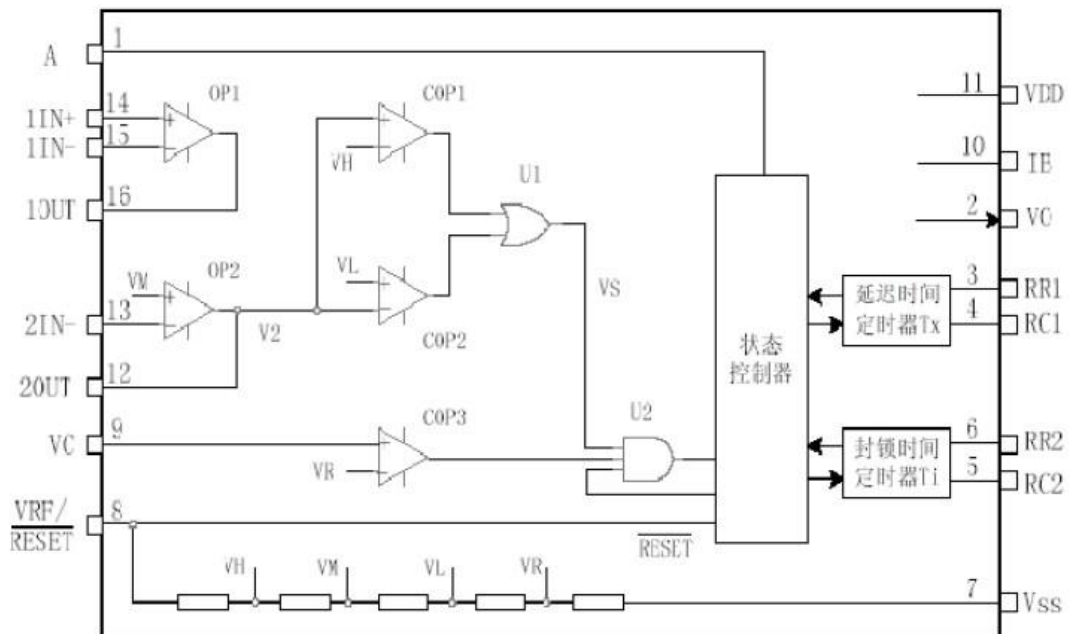
Features

- Low power CMOS technology
- CMOS high input impedance operational amplifiers
- Bi-directional level detector/Excellent noise immunity
- Internal delay time timer and blocking time timer
- Use 16 pin DIP and SOP packages

General Description

The BISS0001 is a sensor signal processing integrated circuits with higher performance. It is accompanied by pyroelectric infrared sensor and a small amount of external components passive pyroelectric infrared switch. It can automatically and quickly turn all kinds of incandescent lamp, fluorescent lamp, a buzzer, automatic door, electric fan, drying machines and other automatic device, particularly suitable for enterprises, hotels, shopping malls, warehouse and families aisles, corridors and other sensitive areas, or for automatic lighting, lighting and alarm system security zone.

Block Diagram

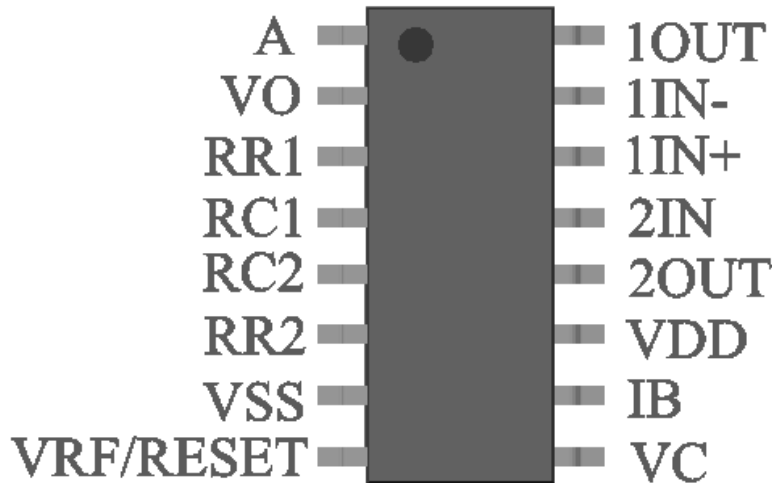




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Package and pin assignment

BISS0001 provides DIP 16 and SOP 16 dual in-line package.



Serial umber	Name	Functional Description
1	A	Select repeatable or non-repeatable trigger . When A = 1 . Allow duplicate trigger; or A = 0; not allowed to repeat the trigger
2	VO	output pin
3	RR1	RR1 output resistor to adjust the delay time ends. (adjust the time lights on to off) $BISS \approx 50000 \times R1 \times C1$
4	RC1	RC1 output capacitor to adjust the delay time end. (adjust the time lights on to off)
5	RC2	RC2 trigger lockout time TI capacitance adjustment end. (adjust the time lights off) $Ti \approx 40 \times R2 \times C2$
6	RR2	RR2 trigger lockout time TI's resistance adjustment end. (adjust the time lights off)
7	VSS	Negative power supply
8	VRF	reference voltage and the reset input. Typically connected toVDD, when connected to can make the timer reset.
9	VC	VC trigger is prohibited. Trigger when $Vc < Vr$. When allowed to trigger $Vc > Vr$. ($Vr = 0.2VDD$)
10	IB	operational amplifier bias current settings
11	VDD	Positive power supply
12	2OUT	The second stage operational amplifier output
13	2IN-	The second stage operational amplifier inverting input
14	1IN+	The first stage operational amplifier inverting input
15	1IN-	The first stage operational amplifier inverting input
16	1OUT	The first stage operational amplifier output